

**PADMABHUSHAN VASAN . DADA PATIL PRATISHTHAN'S COLLEGE OF ENGINEERING, SION E. -MUMBAI-22**  
**TIME TABLE**

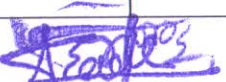
TITLE : ACADEMIC SCHEDULE (FIRST HALF – 2019)		
DEPT : ELECTRONICS ENGINEERING		
W.E.F. : 07/01/2019	CLASS - SE (SEM IV)	CLASSROOM NO-511

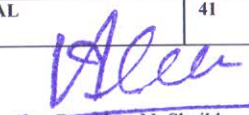
TIME(HRS) DAYS	9:00 to 10:00	10:00 to 11:00	11:00 to 11:20	11:20 to 12:20	12:20 to 13:20	13:20 to 13:50	13:50 to 14:50	14:50 to 15:50	15:50 to 16:50
MONDAY	LCS (/JP)	PCE (/PG)	B	PCE(A) – (/PG) – INSTRU – 507 EDC II (B) – (PM) – EDC – 506		L	EDC-II (PM)	MPA (/KW)	DSD (/TJ)
TUESDAY	PCE (/PG)	AM-IV (AYS)	R	EDC II (A) – (PM) – EDC – 506 MPA (B) – (/KW) – MUC & E – 513		U	DSD-511 (/TJ)	DSD (/TJ)	Counseling & Mentoring – A- 7NZ
WEDNESDAY	EDC-II (PM)	LCS (/JP)	E	MPA (A) – (/KW) – MUC & E – 513 DSD(B) – (/TJ)- R&D-505		N	MPA (/KW)	AM-IV (AYS)	AM-IV-T (AYS))
THURSDAY	EDC-II (PM)	AM-IV (AYS)	A	DSD(A) – (/TJ)- R&D-505 PCE(B) – (/PG) – INSTRU – 507		C	PCE (/PG)	MPA (/KW)	LCS (/JP)
FRIDAY	PCE (/PG)	MPA (/KW)	K	EDC-II-511 (PM)	DSD-511 (/TJ)	H	LCS (/JP)	AM-IV (AYS)	Counseling & Mentoring-B- /PG

SUB	CLASS TEACHER	THEORY	TUTORIAL	PRACTICAL BATCH A	PRACTICAL BATCH B	TOTAL
Applied Mathematics IV- (AM-IV)	AYS-Mr. A Y Shete	04	01	-	-	05
Microprocessors and Applications- (MPA)	/KW- Mrs.Kavita Wagh	04	-	2	2	08
Digital System Design- (DSD)	/TJ- Tejaswita K. Jagtap	04	-	2	2	08
Principles of Communication Engineering- (PCE)	/PG-Mrs. Priya Gupta	04	-	2	2	08
Linear Control Systems- (LCS)	/JP – Mrs. JayshreePawar	04	-	-	-	04
Electronic Devices and Circuits II – (EDCII)	PM –Mr. P.M. Manegopale	04	-	02	02	08
					<b>TOTAL</b>	<b>41</b>

  
22/01/19  
Mr.P.M.Manegopale  
(Time Table Committee Member)

  
Mrs. Gitanjali Korgaonkar  
(Time Table Committee Convener)

  
Mrs. Nilima Zade  
(I/C HOD ELEX)

  
Dr. Alam N. Shaikh  
(PRINCIPAL)

**PADMABHUSHAN VASAI, DADA PATIL PRATISHTHAN'S COLLEGE OF ENGINEERING, SION E. -MUMBAI-22**  
**TIME TABLE**

<b>TITLE : ACADEMIC SCHEDULE (FIRST HALF - 2019 )</b>		
<b>DEPT : ELECTRONICS ENGINEERING</b>		
<b>W.E.F. : 07/01/2019</b>	<b>CLASS - TE (SEM VI)</b>	<b>CLASSROOM NO-406 A</b>

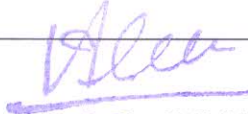
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MONDAY	ESRTP-(A)-(/KW)- MUC & E - 513		B	CCN (/NZ)	ESRTP (/KW)	L	COA (/SP)	VLSI (/PG)	COUNCELLING / MENTORING-/PD
TUESDAY	COA-(A)-(/SP) - SnC I -516		R	VLSI (/PG)	CCN (/NZ)	U	SS (PM)	ESRTP (/KW)	Remedial
WEDNESDAY	VLSI (A)-(/PG)-SnC II-516		E	SS (PM)	COA (/SP)	N	CCN (/NZ)	VLSI (/PG)	Remedial
THURSDAY	CCN (A) -(/NZ) - SnC-I-515		A	COA (/SP)	ESRTP (/KW)	C	CCN (/NZ)	SS (PM)	TPO-PM
FRIDAY	SS-T(A)- (PM)- EDC-506		K	VLSI (/PG)	ESRTP (/KW)	H	SS (PM)	COA (/SP)	Remedial

SUB	CLASS TEACHER	THEORY	TUTORIAL	PRACTICAL BATCH A	TOTAL
Computer Communication and Networks -(CCN)	Mrs. NilimaZade	04	-	02	06
Embedded Systems& Real Time Operating System- (ESRTP)	/KW- Mrs.Kavita Wagh	04	-	02	06
Signals and Systems - (SS)	PM -Mr. P.M. Manegopale	04	02	-	06
VLSI Design-(VLSI)	/PG-Mrs. Priya Gupta	04	-	02	06
Computer Organizationand Architecture-(COA)	/SP-Mrs. Sonali Pakhmode	04	-	02	06
				<b>TOTAL</b>	<b>30</b>

  
 Mr.P.M.Manegopale  
 (Time Table Committee Member)

  
 Mrs. Gitanjali Korgaonkar  
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 Dr. Alam N. Shaikh  
 (PRINCIPAL)

**PADMABHUSHAN VASA. DADA PATIL PRATISHTHAN'S COLLEGE OF ENGINEERING, SION E. -MUMBAI-22**  
**TIME TABLE**


TITLE : ACADEMIC SCHEDULE (FIRST HALF - 2019 )			
DEPT : ELECTRONICS ENGINEERING			
W.E.F. : 07/01/2019	updated 22/01/19	CLASS - BE (SEM VIII)	CLASSROOM NO-406 A , 5011

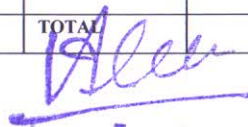
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MONDAY	CMOS VLSI-406 A (/TJ)	ANT-406 A (/SP)	B	MEMS-511 (/JP)	MCOM-511 (VN)	L	ANT (A) - (/NZ) - SnC II - 516 MEMS (B) - (/JP) - R&D -505 CMOS(C) - (/TJ) - SnCI-515		COUNSELLING /MENTORING Batch A-/KW Batch B- /JP Batch C-/SP
TUESDAY	MEMS-406 A (/JP)	CMOS VLSI-406 A (/TJ)	R	MCOM-511 (VN)	ANT-511 (/SP)	U	MCOM(A) - (VN) - INSTRU-507 ANT (B) - (/SP) - SnC II - 516 MEMS (C) - (/JP) - R&D -505		TPO- /TJ
WEDNESDAY	CMOS VLSI-406 A (/TJ)	ANT-406 A (/SP)	E	MCOM-511 (VN)	MEMS-511 (/JP)	N	CMOS(A) - (/TJ) - SnCI-515 MCOM(B) - (VN) - INSTRU-507 ANT (C) - (/SP) - SnC II - 516		Remedial
THURSDAY	ANT-406 A (/SP)	CMOS VLSI-406 A (/TJ)	A	MEMS-511 (/JP)	MCOM-511 (VN)	C	MEMS (A) - (/JP) - R&D -505 CMOS(B) - (/TJ) - SnCI-515 MCOM(C) - (VN) - INSTRU-507		Remedial
FRIDAY	PROJECT		K	PROJECT		H	PROJET		

SUB	CLASS TEACHER	THEORY	PRACTICALS				TOTAL
			A	B	C	D	
CMOS -CMOS VLSI Design	/TJ- Tejaswita K. Jagtap	04	02	02	02	-	10
MEMS - MEMS Technology	/JP-Mrs. Jayshree Pawar	04	02	02	02	-	10
MCOM - Mobile Communication	VN- Prof. V. Nimbalkar	04	02 /PG	02	02	-	10
ANT - Advanced Networking Technologies	/SP- Mrs. Sonali Pakhmode /NZ Mrs. Nilima Zade	04 -	- 02	02 -	02 -	-	08 02
PROJECT							06
						<b>TOTAL</b>	46

  
 Mr. P.M. Manegopale  
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